

Subject index for papers in Volume 47

Each index entry below is accompanied by an author's name and a page number;
the author index contains the title of the paper and the names of coauthors, if any.

Subject	Author	Page	Circuit theory and analysis		
<i>Algorithms</i>			Clocking and clocked storage elements in a multi-gigahertz environment	Oklobdzija	567
High-performance linear algebra algorithms using new generalized data structures for matrices	Gustavson	31	Estimating the efficiency of collaborative problem-solving, with applications to chip design	Wisniewski	77
Mathematical sciences in the nineties	Pulleyblank	89	Low-power circuits and technology for wireless digital systems	Kosonocky	283
Maximizing the system value while satisfying time and energy constraints	Rusu	689	Review and future prospects of low-voltage RAM circuits	Nakagome	525
On greedy algorithms, partially ordered sets, and submodular functions	Dietrich	25	Ultralow-power SRAM technology	Mann	553
Workload-based power management for parallel computer systems	Bradley	703	<i>Clocking</i>		
<i>Analytical models</i>			Clocking and clocked storage elements in a multi-gigahertz environment	Oklobdzija	567
Data-intensive analytics for predictive modeling	Apte	17	<i>CMOS</i>		
Estimating the efficiency of collaborative problem-solving, with applications to chip design	Wisniewski	77	Application of an SOI 0.12- μ m CMOS technology to SoCs with low-power and high-frequency circuits	Plouchart	611
The Common Optimization Interface for Operations Research: Promoting open-source software in the operations research community	Lougee-Heimer	57	Design automation methodology and rf/analog modeling for rf CMOS and SiGe BiCMOS technologies	Harame	139
The mathematics of halftoning	Adler	5	Foundation of rf CMOS and SiGe BiCMOS technologies	Dunn	101
<i>Arrays</i>			Low-power circuits and technology for wireless digital systems	Kosonocky	283
High-performance linear algebra algorithms using new generalized data structures for matrices	Gustavson	31	Prizma switch technology	Engbersen	195
Ultralow-power SRAM technology	Mann	553	<i>Coding theory</i>		
<i>Automata</i>			High-performance linear algebra algorithms using new generalized data structures for matrices	Gustavson	31
Innovations in tape storage automation at IBM	Hellman	445	<i>Communications and communication networks</i>		
<i>Autonomic computing</i>			A direct-conversion receiver integrated circuit for WCDMA mobile systems	Reynolds	337
The IBM Virtual Tape Server: Making tape controllers more autonomic	Kishi	459	Broadband radio LANs and the evolution of wireless beyond 3G	Chevillat	327
<i>Circuit and device technology</i>			Data aggregation architectures for single-chip SDH/SONET framers	Clauberg	211
Clocking and clocked storage elements in a multi-gigahertz environment	Oklobdzija	567	Design of optical communications data links	Pepeljugoski	223
Design automation methodology and rf/analog modeling for rf CMOS and SiGe BiCMOS technologies	Harame	139	Developing integrated antenna subsystems for laptop computers	Liu	355
Estimating the efficiency of collaborative problem-solving, with applications to chip design	Wisniewski	77	Evolution of optical subassemblies in IBM data communication transceivers	Trehwella	251
Low-power circuits and technology for wireless digital systems	Kosonocky	283	IBM PowerNP network processor: Hardware, software, and applications	Allen	177
Review and future prospects of low-voltage RAM circuits	Nakagome	525	SiGe BiCMOS integrated circuits for high-speed serial communication links	Friedman	259

<i>Complexity</i>				<i>Image processing</i>		
Ergodic theory of one-dimensional dynamics	Martens	67		The mathematics of halftoning	Adler	5
<i>Computational methods</i>				<i>Imaging technology</i>		
The Common Optimization Interface for Operations Research: Promoting open-source software in the operations research community	Lougee-Heimer	57		The mathematics of halftoning	Adler	5
<i>Computer applications</i>				<i>Integrated circuit design</i>		
The Common Optimization Interface for Operations Research: Promoting open-source software in the operations research community	Lougee-Heimer	57		A direct-conversion receiver integrated circuit for WCDMA mobile systems	Reynolds	337
<i>Computer architecture</i>				SiGe BiCMOS integrated circuits for high-speed serial communication links	Friedman	259
Broadband radio LANs and the evolution of wireless beyond 3G	Chevillat	327		<i>Integrated circuits</i>		
Comparison of analytic performance models using closed mean-value analysis versus open-queueing theory for estimating cycles per instruction of memory hierarchies	Matick	495		SiGe BiCMOS integrated circuits for high-speed serial communication links	Friedman	259
Data aggregation architectures for single-chip SDH/SONET framers	Clauberg	211		<i>Knowledge systems</i>		
Prizma switch technology	Engbersen	195		Data-intensive analytics for predictive modeling	Apte	17
<i>Design automation</i>				<i>Large-scale computing</i>		
Design automation methodology and rf/analog modeling for rf CMOS and SiGe BiCMOS technologies	Harame	139		On the performance and use of dense servers	Felter	671
<i>Device design</i>				<i>Local area networks (LANs)</i>		
Application of an SOI 0.12- μ m CMOS technology to SoCs with low-power and high-frequency circuits	Plouchart	611		Design of optical communications data links	Pepeljugoski	223
Clocking and clocked storage elements in a multi-gigahertz environment	Oklobdzija	567		<i>Magnetic head design</i>		
Review and future prospects of low-voltage RAM circuits	Nakagome	525		Hard-disk-drive technology flat heads for linear tape recording	Biskeborn	385
<i>Ferrites</i>				Head reliability of AMR sensors based on thermal stress tests	Iben	415
Hard-disk-drive technology flat heads for linear tape recording	Biskeborn	385		Six orders of magnitude in linear tape technology: The one-terabyte project	Childers	471
<i>Finite element analysis</i>				Steady-state thermal characteristics of AMR read/write heads used in tape storage drives	Iben	401
Steady-state thermal characteristics of AMR read/write heads used in tape storage drives	Iben	401		<i>Magnetic recording</i>		
<i>Formal methods</i>				Fifty years of IBM innovation with information storage on magnetic tape	Bradshaw	373
On greedy algorithms, partially ordered sets, and submodular functions	Dietrich	25		Hard-disk-drive technology flat heads for linear tape recording	Biskeborn	385
<i>Fractals</i>				Head reliability of AMR sensors based on thermal stress tests	Iben	415
Grammatical evolution to design fractal curves with a given dimension	Ortega	483		Innovations in tape storage automation at IBM	Hellman	445
<i>Germanium</i>				LTO: A better format for mid-range tape	Jaquette	429
Foundation of rf CMOS and SiGe BiCMOS technologies	Dunn	101		Six orders of magnitude in linear tape technology: The one-terabyte project	Childers	471
<i>Graphics</i>				Steady-state thermal characteristics of AMR read/write heads used in tape storage drives	Iben	401
The mathematics of halftoning	Adler	5		<i>Magnetic tape</i>		
<i>Heterostructures and heterojunctions</i>				Fifty years of IBM innovation with information storage on magnetic tape	Bradshaw	373
Foundation of rf CMOS and SiGe BiCMOS technologies	Dunn	101		Hard-disk-drive technology flat heads for linear tape recording	Biskeborn	385
				Innovations in tape storage automation at IBM	Hellman	445
				LTO: A better format for mid-range tape	Jaquette	429

Six orders of magnitude in linear tape technology: The one-terabyte project	Childers	471	Design and validation of a performance and power simulator for PowerPC systems	Shafi	641
<i>Magnetoresistance</i>			Estimating the efficiency of collaborative problem-solving, with applications to chip design	Wisniewski	77
Hard-disk-drive technology flat heads for linear tape recording	Biskeborn	385	Mathematical sciences in the nineties	Pulleyblank	89
Head reliability of AMR sensors based on thermal stress tests	Iben	415	Maximizing the system value while satisfying time and energy constraints	Rusu	689
Six orders of magnitude in linear tape technology: The one-terabyte project	Childers	471	New methodology for early-stage, microarchitecture-level power-performance analysis of microprocessors	Brooks	653
Steady-state thermal characteristics of AMR read/write heads used in tape storage drives	Iben	401			
<i>Materials technology</i>			<i>Multimedia</i>		
Hard-disk-drive technology flat heads for linear tape recording	Biskeborn	385	Fine-grain real-time reconfigurable pipelining	Kim	599
<i>Mathematical functions and techniques</i>			<i>Networks</i>		
Ergodic theory of one-dimensional dynamics	Martens	67	Broadband radio LANs and the evolution of wireless beyond 3G	Chevillat	327
Grammatical evolution to design fractal curves with a given dimension	Ortega	483	Data aggregation architectures for single-chip SDH/SONET framers	Clauberg	211
<i>Mathematics</i>			IBM PowerNP network processor: Hardware, software, and applications	Allen	177
Ergodic theory of one-dimensional dynamics	Martens	67	SiON high-refractive-index waveguide and planar lightwave circuits	Bona	239
Grammatical evolution to design fractal curves with a given dimension	Ortega	483	<i>Operations research</i>		
High-performance linear algebra algorithms using new generalized data structures for matrices	Gustavson	31	The Common Optimization Interface for Operations Research: Promoting open-source software in the operations research community	Lougee-Heimer	57
Mathematical sciences in the nineties	Pulleyblank	89			
On greedy algorithms, partially ordered sets, and submodular functions	Dietrich	25	<i>Optical interconnections</i>		
<i>Mathematics (applied)</i>			Design of optical communications data links	Pepeljugoski	223
Estimating the efficiency of collaborative problem-solving, with applications to chip design	Wisniewski	77	Evolution of optical subassemblies in IBM data communication transceivers	Trehwella	251
Mathematical sciences in the nineties	Pulleyblank	89			
The mathematics of halftoning	Adler	5	<i>Optimization</i>		
<i>Memory, random-access</i>			Balancing hardware intensity in microprocessor pipelines	Zyuban	585
Review and future prospects of low-voltage RAM circuits	Nakagome	525	Maximizing the system value while satisfying time and energy constraints	Rusu	689
Ultralow-power SRAM technology	Mann	553	New methodology for early-stage, microarchitecture-level power-performance analysis of microprocessors	Brooks	653
<i>Microprocessor systems and applications</i>			Workload-based power management for parallel computer systems	Bradley	703
An innovative low-power high-performance programmable signal processor for digital communications	Moreno	299	<i>Optoelectronic device technology</i>		
Balancing hardware intensity in microprocessor pipelines	Zyuban	585	Evolution of optical subassemblies in IBM data communication transceivers	Trehwella	251
IBM PowerNP network processor: Hardware, software, and applications	Allen	177	SiON high-refractive-index waveguide and planar lightwave circuits	Bona	239
New methodology for early-stage, microarchitecture-level power-performance analysis of microprocessors	Brooks	653			
On the performance and use of dense servers	Felter	671	<i>Packaging</i>		
<i>Models and modeling</i>			Developing integrated antenna subsystems for laptop computers	Liu	355
Data-intensive analytics for predictive modeling	Apte	17			

Performance analysis

- Comparison of analytic performance models using closed mean-value analysis versus open-queueing theory for estimating cycles per instruction of memory hierarchies Matick 495

- High-performance linear algebra algorithms using new generalized data structures for matrices Gustavson 31

Power management

- An innovative low-power high-performance programmable signal processor for digital communications Moreno 299

- Application of an SOI 0.12- μ m CMOS technology to SoCs with low-power and high-frequency circuits Plouchart 611

- Balancing hardware intensity in microprocessor pipelines Zyuban 585

- Clocking and clocked storage elements in a multi-gigahertz environment Oklobdzija 567

- Design and validation of a performance and power simulator for PowerPC systems Shafi 641

- Fine-grain real-time reconfigurable pipelining Kim 599

- Low-power circuits and technology for wireless digital systems Kosonocky 283

- Maximizing the system value while satisfying time and energy constraints Rusu 689

- New methodology for early-stage, microarchitecture-level power-performance analysis of microprocessors Brooks 653

- On the performance and use of dense servers Felter 671

- The design and application of the PowerPC 405LP energy-efficient system-on-a-chip Nowka 631

- Workload-based power management for parallel computer systems Bradley 703

Printing technology

- The mathematics of halftoning Adler 5

Robotics

- Innovations in tape storage automation at IBM Hellman 445

Semiconductor devices

- A direct-conversion receiver integrated circuit for WCDMA mobile systems Reynolds 337

- Design automation methodology and rf/analog modeling for rf CMOS and SiGe BiCMOS technologies Harame 139

- Developing integrated antenna subsystems for laptop computers Liu 355

Semiconductor technology

- SiON high-refractive-index waveguide and planar lightwave circuits Bona 239

Servers

- On the performance and use of dense servers Felter 671

- Workload-based power management for parallel computer systems Bradley 703

Signal processing

- An innovative low-power high-performance programmable signal processor for digital communications Moreno 299

Silicon-germanium

- A direct-conversion receiver integrated circuit for WCDMA mobile systems Reynolds 337

- Design automation methodology and rf/analog modeling for rf CMOS and SiGe BiCMOS technologies Harame 139

- Foundation of rf CMOS and SiGe BiCMOS technologies Dunn 101

- SiGe BiCMOS integrated circuits for high-speed serial communication links Friedman 259

Silicon-on-insulator (SOI)

- Application of an SOI 0.12- μ m CMOS technology to SoCs with low-power and high-frequency circuits Plouchart 611

Simulation

- Design and validation of a performance and power simulator for PowerPC systems Shafi 641

- Design of optical communications data links Pepeljugin 223

- New methodology for early-stage, microarchitecture-level power-performance analysis of microprocessors Brooks 653

- On the performance and use of dense servers Felter 671

Storage (computer) devices and systems

- Fifty years of IBM innovation with information storage on magnetic tape Bradshaw 373

- Six orders of magnitude in linear tape technology: The one-terabyte project Childers 471

- Tape management in a storage networking environment Deicke 453

- The IBM Virtual Tape Server: Making tape controllers more autonomic Kishi 459

Storage hierarchies

- Comparison of analytic performance models using closed mean-value analysis versus open-queueing theory for estimating cycles per instruction of memory hierarchies Matick 495

- Fifty years of IBM innovation with information storage on magnetic tape Bradshaw 373

- Six orders of magnitude in linear tape technology: The one-terabyte project Childers 471

- Tape management in a storage networking environment Deicke 453

- The IBM Virtual Tape Server: Making tape controllers more autonomic Kishi 459

Switches

- Prizma switch technology Engbersen 195

<i>Switching circuits</i>			
Prizma switch technology	Engbersen	195	
<i>System-on-a-chip (SoC)</i>			
The design and application of the PowerPC 405LP energy-efficient system-on-a-chip	Nowka	631	
<i>Testing</i>			
Head reliability of AMR sensors based on thermal stress tests	Iben	415	
<i>Topology</i>			
Ergodic theory of one-dimensional dynamics	Martens	67	